

# 1 **Process Engineering of Semi-transparent DSSC Modules and Panel Incorporating an Organic** 2 **Sensitizer**

3 *Luigi Vesce, Paolo Mariani, Massimo Calamante, Alessio Dessì, Alessandro Mordini, Lorenzo*  
4 *Zani\*, and Aldo Di Carlo\**

7 5 Dr. L. Vesce, Dr. P. Mariani, Prof. A. Di Carlo  
8 6 CHOSE – Centre for Hybrid and Organic Solar Energy, Department of Electronic Engineering,  
9 7 University of Rome “Tor Vergata”, Via del Politecnico 1, 00133 Rome – Italy  
10 8 E-mail: aldo.dicarlo@uniroma2.it

13 9 Dr. M. Calamante, Dr. A. Dessì, Dr. A. Mordini, Dr. L. Zani  
14 10 Institute of Chemistry of Organometallic Compounds (CNR-ICCOM), Via Madonna del Piano 10,  
15 11 50019 Sesto Fiorentino - Italy  
16 12 E-mail: lorenzo.zani@iccom.cnr.it

18  
19 13 Dr. M. Calamante, Dr. A. Mordini  
20 14 Department of Chemistry “U. Schiff”, University of Florence, Via della Lastruccia 13, 50019 Sesto  
21 15 Fiorentino - Italy

23  
24 16 Prof. A. Di Carlo  
25 17 Institute of Structure of Matter (CNR-ISM), via del Fosso del Cavaliere 100, 00133 Rome - Italy

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28 18 **Keywords:** Dye sensitized solar cell, PV module, PV panel, stability studies, semi-transparent,  
29 19 BIPV.

## 31 **Abstract**

33 20 Among the photovoltaic technologies adopted in the building-integrated photovoltaics (BIPV)  
34 21 sector, Dye-Sensitized Solar Cells (DSSCs) appear very attractive because of unique features like  
35 22 tunable color and good transparency. However, the prospect of their low-cost fabrication is realistic  
36 23 only if reliable and scalable processes under real manufacturing conditions (*i.e.* pilot line and/or  
37 24 plant factory) are designed, developed and optimized for large-area, efficient and stable devices.  
38 25 Here, we show a highly reproducible process based on the deposition of different inks by screen-  
39 26 printing technique to realize twenty modules (400 cm<sup>2</sup>) and one panel (0.2 m<sup>2</sup>) incorporating an  
40 27 organic sensitizer. Module design considers the resistive losses caused by the electron transport, the  
41 28 durability of the device and the aspect ratio (more than 70%). The module champion efficiency was  
42 29 5.1% with 35.7% transparency (AVT - Average Visible Transmittance), and its stability (ISOS-D-2  
43 30 and ISOS-L-1) was determined to be >1000 h. The modules showed no structural failures,  
44 31 electrolyte leakage, grids corrosion, empty liquid electrolyte zones and moiré patterns on the sealing  
45 32 part. The consistency of the gap between photo- and counter-electrode before and after the stress  
46 33 was demonstrated. An industrial lamination process to realize a panel with an outdoor efficiency of  
47 34 2.7% at 60 °C tilt angle was adopted.  
48 35

## 1. Introduction

Dye Sensitized Solar Cells (DSSC) have been the first third-generation photovoltaic (PV) technology developed to commercialization level [1-4]. Although device efficiency (PCE, power conversion efficiency) is still low (13% for small area cells and 8.8% for sub-modules) if compared to the well-known silicon-based technology (more than 26%) [5,6], some peculiar features make DSSCs very attractive for building-integrated photovoltaics (BIPVs) [7-10], such as: control of device color [11], transparency in the visible range [12,13], low dependence of performance on the light angle [14,15], better response in diffuse light compared to semiconductors-based PV technologies [16] and environmental sustainability [17]. BIPV market is expected to grow by 150% in the next decades as a result, among others, of the renewable energy integration policies of the European Union [17,18]. In this regard, buildings equipped with BIPV devices could be viewed like small and distributed power plants with annual near-zero energy balance [19,20]. Accordingly, modules and panels must be highly transparent (transmittance range between 25% ad 38%) and efficient, to act as glazing building envelopes, curtain-wall façades or large skylights [21-25]. Among PV technologies applicable to building integration, c-Si has very high efficiency and the mutual distance between individual cells can be settled to have some transparency, but the pattern produces shades not comfortable for the internal living [19,22]. Besides, tilt angle and low irradiation levels complicate their integration in architectural glazing.

In DSSC technology, the TiO<sub>2</sub> thickness and adopted dye molecule play a key-role to define the optical properties of the device [4,26-28]. In particular, the chosen dye must present some essential features [29], including: absorption spectrum covering the whole visible region, presence of appropriate anchoring groups for binding to the semiconductor surface, correct energy level alignment with both the semiconductor and the redox mediator, sufficient chemical and photo-stability. The superposition of electrolyte and dye spectra is responsible of the DSSC color, while TCO (transparent conductive oxide), dye, electrolyte, platinum (counter electrode material), and TiO<sub>2</sub> particle size and thickness all influence the transparency of the device (e.g. small particle size and low thickness give high transparency, but the corresponding devices usually present a lower current density respect to high thickness or scattering layer based cells) [9,13,30-33]. Semi-transparent DSSC based on dyes with symmetrical NIR-selective heptamethyne cyanine and donor- $\pi$ -conjugated-bridge-acceptor (D- $\pi$ -A) structure got efficiencies of 3.1% and 4.17%, respectively [34,35].

The industrialization and commercialization of DSSC technology is feasible if different cells are connected to realize a Dye Sensitized Solar Module (DSSM) [4]. This complicates the

1 manufacturing, performance and stability respect to lab cells. The TCO glass substrates limit the  
2 performance of DSSMs, because of non-negligible sheet resistance (6-10 ohm/sq) that can induce  
3 large ohmic losses when the current of the cell increases [3]. This issue is overcome by depositing  
4 conducting grids onto the conductive substrates [36] or by realizing modules with narrow cells  
5 connected in series (monolithic, W, Z) or parallel/monolithic [4,16,37-41]. A layout and  
6 interconnection optimization process is thus needed to find the best suitable single cell width  
7 according to illumination, sheet resistance, fabrication technology, efficiency and durability [41-43].  
8 Finally, sealing of the individual cells in modules is critical, since any sealant failure can severely  
9 limit the module lifetime [44].

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11 In this context, the scaling up from small lab cells to modules and panels by adopting simple and  
12 low cost fabrication processes is of paramount importance, as for other third generation PV  
13 technologies [3,22,45,46]. DSSMs and panels (DSSPs) can be fabricated by methods inherited from  
14 other industrial sectors and the manufacturing process should achieve a reliable/high throughput  
15 production, good performance and stability [16,47,48]. In any case, most of the procedure details are  
16 hidden because of the industrial relevance of the upscaling process. In the last decades, some  
17 examples of semi-transparent DSSMs have been reported. Sastrawan *et al.* reported a 900 cm<sup>2</sup>  
18 meander-shaped semi-transparent module with an efficiency of 3.1% [42]. Takeda *et al.* showed a  
19 transparent monolithic serial-connected device (9.5×9.5 cm<sup>2</sup>) by using Pt-loaded In<sub>2</sub>O<sub>3</sub>:Sn  
20 nanoparticles and an insulator composed of SiO<sub>2</sub> particles [49]. In 2014, Solaronix installed 300 m<sup>2</sup>  
21 of DSSPs (W-connected modules) on Swiss Tech Convention Center façade at EPFL campus [50].  
22 The single module was 35×50 cm<sup>2</sup> with 1.05% efficiency based on active area. Despite the  
23 impressive installation, many modules appeared degraded by different routes [4]. Vesce *et al.*  
24 showed a series of Z-connected 600 cm<sup>2</sup> module fabricated in the Italian Dyepower pilot line  
25 facility with Dyenamo D35 dye [3,4,17]. The efficiency and the transparency were 5.6% and 30%,  
26 respectively. The devices underwent UV pre-conditioning, humidity freeze and damp heat tests  
27 (IEC 61215). Moreover, the environmental profile was compared to other PV technologies [17].  
28 H.Glass and Dongjin Semichem realized huge DSSC installations on Science Tower in Austria and  
29 on Roskilde University in Denmark, respectively [4]. Lee *et al.* tested a semi-transparent, series  
30 connected 900 cm<sup>2</sup> module from a Korean company with an efficiency of 2.76% [51]. Kim *et al.*  
31 reported Z-type 900 cm<sup>2</sup> module with Z907 dye and 3.19% efficiency [52]. Godfroy *et al.* showed a  
32 23 cm<sup>2</sup> semi-transparent module (W-type connection) with 8.7% efficiency based on active area and  
33 26% AVT (Average Visible Transmittance) by adopting a benzothiadiazole-based photosensitizer  
34 [53]. According to this short overview, just in two cases the AVT [3,17,53] values have been reported  
35 (no data about the color rendering index – CRI) and no specific details about the fabrication

1 procedure and the aspect ratio of the devices were provided. Moreover, in agreement with the  
2 observations of Muñoz-Garcia *et al.* [4], we found a lack of published stability tests of semi-  
3 transparent modules.  
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6 In this paper, we show the detailed fabrication procedure of twenty semi-transparent DSSMs (400  
7 cm<sup>2</sup> each) with high reproducibility, and of one DSSP (0.2 m<sup>2</sup>), all containing the dye TTZ5, a  
8 thiazolo[5,4-*d*]thiazole (TzTz) organic sensitizer with strong visible light absorption [54]. Modules  
9 are designed by considering the trade-off between low losses and device sturdiness. The finest  
10 module has an efficiency of 5.1% on active area (70% aspect ratio) and a transparency of 35.7% on  
11 aperture area (315.4 cm<sup>2</sup>) that is appropriate for BIPV application [23]. According to ISOS-D-2 and  
12 ISOS-L-1 standard tests, the modules are stable for more than 1000 h at 85 °C and under light  
13 soaking without any electrolyte leakage, collectors corrosion or delamination. Nine modules are  
14 assembled in three strings and then in one laminated panel by an industrially-compatible process.  
15 The maximum efficiency of the panel under outdoor illumination conditions at 60° tilt angle is  
16 2.7%.  
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## 29 **2. Results and Discussion**

### 30 **2.1 Choice of the organic dye and synthesis**

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32 Compared to typical metalorganic sensitizers, such as Ru-bipyridine complexes, organic dyes  
33 present some potential advantages in the context of BIPV applications, such as: the simple and  
34 scalable synthetic routes, the absence of any heavy, toxic or critical metal (whose supply could be  
35 limited in the case of large-scale module production), the high molar attenuation coefficients in the  
36 visible region and the possibility to easily tune their electronic properties by precise structural  
37 modifications, making several color nuances accessible starting from the same general structure [4].  
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39 In this regard, dye TTZ5, reported for the first time by some of us in 2014, stood out for its broad  
40 light absorption in the visible region ( $\lambda_{\max} = 487$  nm and  $\lambda_{\text{onset}} > 650$  nm on TiO<sub>2</sub>) and its above-  
41 average molar attenuation coefficient ( $\epsilon = 9.41 \times 10^4$  M<sup>-1</sup> cm<sup>-1</sup>, measured in THF solution),  
42 properties that made it a very suitable candidate as a photosensitizer for semi-transparent thin-film  
43 DSSCs [55]. Small-scale DSSCs (active area = 0.25 cm<sup>2</sup>) containing a semi-transparent 5.5  $\mu\text{m}$ -thick  
44 TiO<sub>2</sub> layer photosensitized with TTZ5 and HPE (High Performance Electrolyte, Great Cell Solar)  
45 electrolyte, showed a remarkable light-to-energy conversion efficiency of 7.39% (FF = 63%),  
46 without the need of any coadsorbent, such as chenodeoxycholic acid (CDCA), outperforming a  
47 standard organic dye (D5) and the Ru-based sensitizer Z907. Dye TTZ5 confirmed its excellent  
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1 photovoltaic performances (PCE = 6.20–6.35%, FF = 53%) in larger area cells (active area: 3.6  
2 cm<sup>2</sup>) with even smaller TiO<sub>2</sub> thicknesses (3–5 μm). Cells built with HSE (High Stability Electrolyte,  
3 Great Cell Solar) electrolyte showed excellent stability, steadily around 6%, over a period of 1000  
4 h, during which they were stored in the dark at 85 °C [54]. It is noteworthy that these results were  
5 obtained keeping device fabrications as simple as possible, without any TiO<sub>2</sub> blocking layer and  
6 light-scattering layer, and employing a commercial electrolyte solution containing the simplest  
7 redox couple, I<sup>-</sup>/I<sub>3</sub><sup>-</sup>. For these reasons, TTZ5 appeared a promising candidate dye for larger-scale  
8 BIPV applications.

9 Recently, we completely revised the synthesis of TTZ5 [56] to achieve a gram-scale preparation of  
10 the dye, reduce the amount of waste produced and its cost, and shorten the number of synthetic  
11 steps in comparison to the original synthesis (Figure S1) [55]. Following the reported procedure (see  
12 Supporting Information), we were able to prepare TTZ5 dye on gram-scale, enough for the  
13 fabrication of the modules and panel described in the following paragraph.

## 14 15 **2.2 Fabrication of dye sensitized solar modules and panel**

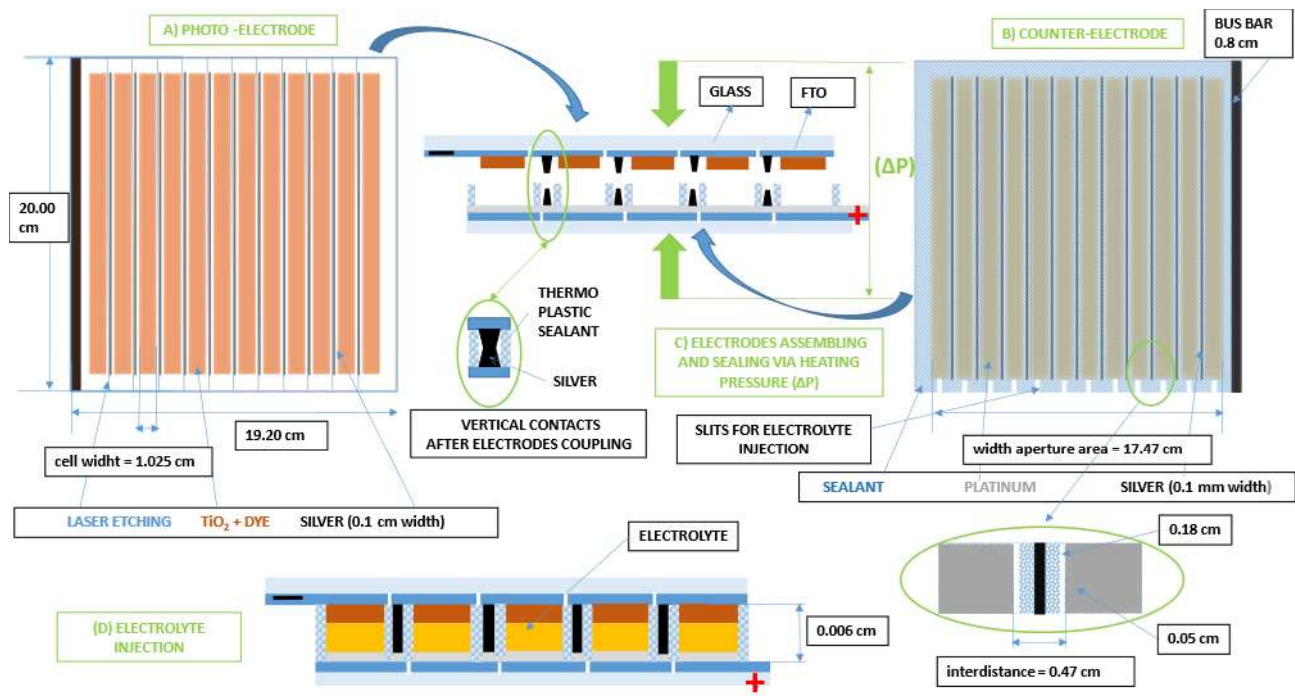
16 Device performances are limited by sheet resistance of the transparent conductive substrates when  
17 scaling up from lab cell to module. The resistive losses are mitigated by patterning the substrate in  
18 several interconnected cells [3]. Among the different module architectures (parallel, series  
19 monolithic, series W-type and series Z-type connection), we adopted the series Z-type connection  
20 due to the high voltage of the resulting device, as well as the uniform and reliable power output  
21 over large area in different illumination and temperature conditions [57]. Conversely, the parallel  
22 connection has a low working potential and high output current that leads to high power losses,  
23 while W-type connection suffers of imbalanced cells (mainly concerning the photocurrents), since  
24 half of them receives sunlight from the counter-electrode side [40]. In the series Z-type connection,  
25 the charge collection from the cells and the electrical connections are guaranteed by a conducting  
26 material placed between the sandwiched conductive substrates. Conductors and cells must be  
27 effectively insulated to avoid metal corrosion and unwanted mass transport between the cells,  
28 respectively [16,58]. The presence of spikes in the metal conductor, or the volume expansion of the  
29 electrolyte, could affect the device sealing, pushing the electrodes away from each other with a  
30 subsequent electrolyte leakage [59,60]. The vertical connections and sealant can also cause some  
31 troubles during the assembling process of the modules, since tolerances for their height variations  
32 are usually tight [3]. The best interconnection strategy to fabricate efficient and stable Z-type DSSC  
33 modules according to ISOS (International Summit on Organic Photovoltaic Stability) and IEC

(International Electrotechnical Commission) standards is still an open technological issue [60–62]. Few works showed the possibility to mitigate the aggressive action of the electrolyte by using grids of metals different than silver, such as nickel, chromium, tungsten and molybdenum in form of wires, or protecting silver by adopting processes different from printing [63–66]. Hopkins *et al.* adopted a Ti-based compound composed by 45  $\mu\text{m}$  Ti particles and 5  $\mu\text{m}$  W particles in a polymer matrix [67]. Since the compound was less sensitive to electrolyte corrosion compared to Ag, a new design without protection for the grids could be used. However, the resulting vertical connections were less conductive than those based on Ag, causing a reduction of module efficiency. Tulloch *et al.* protected the metallic conductors from the tri-iodide/iodide couple by applying chemically inert nonmetallic materials, such as TiN, ZrN or boron carbide [68]. The manufacturing complexity was increased, and the module lost the bifacial feature to be illuminated from photo- and counter-electrode [69,70]. Recently, we showed how graphene can act as a collector in DSSM [60]. In that case, we increased the number and the width of the cells in the substrate, but the contact resistance between FTO and graphene was still high.

We designed the device according to the equivalent circuit models of DSSC and DSSM developed on the electronic simulation software Orcad PSpice, as reported in a recent publication [3]. To evaluate the optimized layout of a Z-type connection, we used the single cell electrical parameters from previous published works by our groups that adopted the same materials reported in the experimental section [54,71]. The module dimensions, including single cell area, conductors' width, inter-distance between two adjacent cells and aspect ratio are reported in Table 1 and Figure 1. The layout also considers the space for a robust sealing all around the aperture area and the conductors.

**Table 1.** Device geometrical details. Parameters defined as in ref.[3].

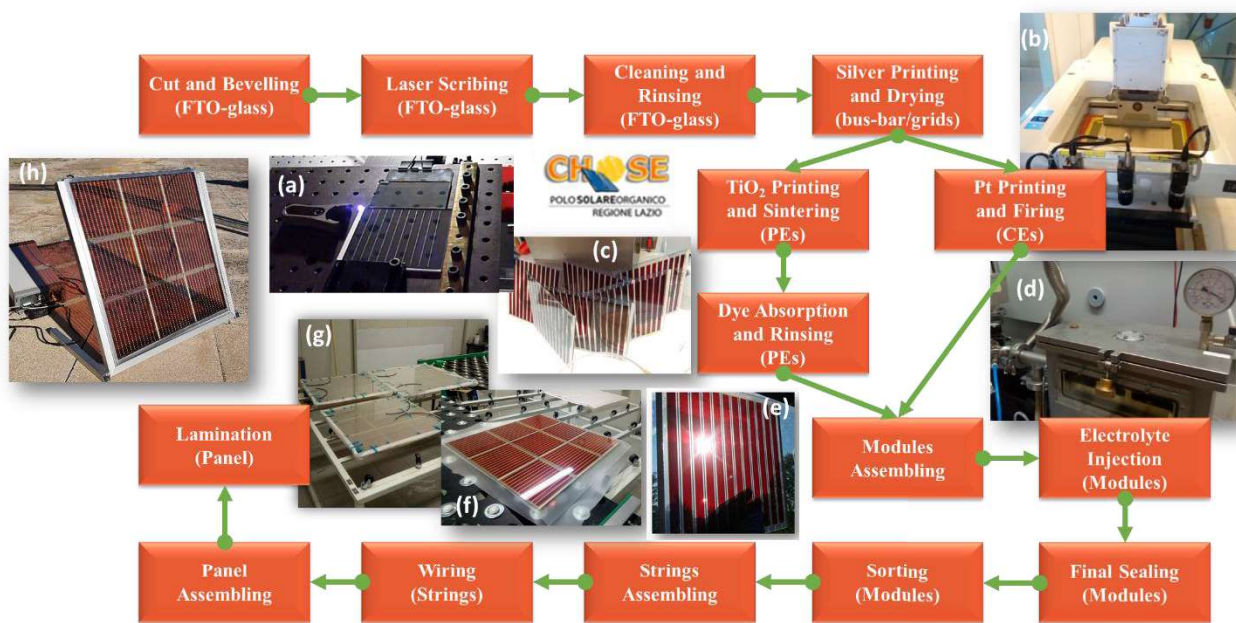
<b>Module size (cm<sup>2</sup>)</b>	20×20
<b>Substrate size (cm<sup>2</sup>)</b>	19.2×20
<b>Number of cells</b>	12
<b>Single cell active area (cm<sup>2</sup>)</b>	18.5 (10.25 mm × 180.5 mm)
<b>Module active area (cm<sup>2</sup>)</b>	222
<b>Aperture area (cm<sup>2</sup>)</b>	315.4
<b>Aspect ratio (%)</b>	70.4
<b>Inter-distance (mm)</b>	4.7
<b>Conductor width (mm)</b>	1



**Figure 1.** DSSMs layout and series Z-type connection. A) Photo-electrode (laser scribes in sky blue,  $\text{TiO}_2$  + dye in brown, silver in black); B) Counter-electrode (platinum in grey, thermoplastic sealant in textured blue); C) The two electrodes just before the coupling; D) The two electrodes after the assembling with the injected electrolyte (in yellow).

The manufacturing process of 20 modules is depicted in Figure 2 and the details are reported in the Experimental Section. Each module is composed by two FTO (fluorinated tin oxide)-covered glass sheets (2.2 mm thick), one for the photo-electrode (PE) and one for the counter-electrode (CE). The laser scribing step was used to insulate adjacent cells by removing a narrow line of FTO [72] (Figure 2,a). The next steps highlighted the advantage of DSSC technology by employing the well-known, high throughput and cheap screen-printing technique to deposit silver conductors and bus bars on both electrodes,  $\text{TiO}_2$  film on PEs and Pt clusters on CEs [3,58] (Figure 2,b). The homogeneity of each of these materials is mandatory to ensure that all cells in the module have comparable performances. The first printed material was a low-resistivity conductive silver paste for external bus bars and collecting/interconnection grids. The latter collect charges produced by each single cell and connect it to the next cell. The width of the conductors was optimized according to the trade-off between the highest possible active area, sufficient space for sealant and maximum power output [3]. Electrodes were fired at 120 °C for 30 min to dry the ink before the deposition of  $\text{TiO}_2$  and Pt pastes. The PEs (Ag +  $\text{TiO}_2$ ) were sintered at 500 °C for 30 min (2.5 h ramping) and the CEs (Ag + Pt) were fired at 480 °C for 30 min (2 h ramping), obtaining a thickness of 31  $\mu\text{m}$  and 5.5  $\mu\text{m}$  for

Ag grids and TiO<sub>2</sub> layer, respectively. In both cases, the thermal process was optimized to avoid any glass deformation that can have detrimental effects on the reliability of the devices [59]. The sintering process of the PE is necessary to strengthen the electrochemical bonds between TiO<sub>2</sub> nanoparticles and to burn organic solvents and binders present in the paste [13,73]. The firing step of the CE permits to thermally reduce the Pt precursor-based paste to the elementary form of the metal [72,74]. During the dipping step (15 h), the nanocrystalline semiconductor films adsorbed a large amount of the organic TTZ5 dye molecules, increasing the harvesting efficiency of solar energy [71] (Figure 2,c). Since the PEs sensitization step is the bottleneck of the process [17], dye drops can be generated on demand by an ink jet printer, avoiding waste and reducing material consumption [75].



**Figure 2.** Process flow for DSSMs e DSSP fabrication. The full process is in ambient environment. (a) FTO-glass laser scribing; (b) ink screen-printing; (c) dye sensitized PEs; (d) electrolyte injection chamber; (e) DSSM; (f) panel assembling; (g) panel pre-lamination stage; (h) DSSP.

In the assembling step, the PE and the CE were coupled and pressed together with a printing press system. A thermoplastic sealant was added at the interface of the substrates to avoid electrolyte leakage (possibly causing corrosion of the metal contacts) and solvent evaporation. The process was composed of four steps: 1) Bynel™ thermoplastic foil cutting according to active areas, grids, bus bar, electrolyte injection slits and substrate edges; 2) positioning of the foil on the CE and pressing under high temperature; 3) alignment of PE and CE and pressing under high temperature till thermoplastic melting; 4) slits sealing by pressure and temperature after electrolyte injection. In the first step, a small slit was shaped for the subsequent electrolyte injection process (Figure 1). Since the slit must be kept open until the third step, precise control of temperature and pressure



1 parameters in the following step is mandatory. Indeed, during the second step, the thermoplastic foil  
2 must stick on CE without melting, as instead necessary in the third step, in which the two electrodes  
3 were sealed together and aligned to series connect the grids (Figure 1), and the thermoplastic foil  
4 became transparent after melting. In the fourth step, only the edge of the module with the slits was  
5 positioned under the pressure system to avoid thermal and pressure overexposure of the polymer in  
6 the main part of the device.  
7

8 The surface occupied by the thermoplastic gasket is designed to avoid any overlapping with cells  
9 and conductors (Figure 1). Moreover, the thickness of the sealant defines the electrodes gap and  
10 was adjusted to withstand thermal expansion of the liquid electrolytes and substrates traction. In  
11 detail, the thicknesses of the thermoplastic foil and of the silver conductor on one electrode are 80  
12  $\mu\text{m}$  (about 60  $\mu\text{m}$  after the assembling process, *i.e.* the spacing of the electrodes) and about 30  $\mu\text{m}$ ,  
13 respectively. The designed and obtained thicknesses were optimized to guarantee the electrical  
14 series interconnection and uniform gaps between PE and CE for each cell in the module. According  
15 to literature reports, assembling modules with cells wider than 18 mm was required to obtain  
16 uniform thickness of the internal module gap and thermal reliability <sup>[52,76]</sup>. In our work, we obtained  
17 both a homogeneous gap (before and after the thermal stress, Figure S2) and stable module devices  
18 (see below, Section 2.4) with a cell width of 10.25 mm. We report the gap/thickness values in 9  
19 distributed points of a dummy module before and after the thermal stress (Figure S2). The dummy  
20 module experienced all the process steps and relative stresses (thermal and mechanical) of the  
21 working modules except for the dye dipping and Pt deposition, to make the measurement possible  
22 (see Experimental Section). In Figure S2, it's easy to appreciate the high homogeneity ( $59 \pm 2 \mu\text{m}$ )  
23 and the consistency of the gap after a thermal stress at 85 °C for more than 1000 h (only approx. 1  
24  $\mu\text{m}$  average difference compared to the initial value). Sealing of the device is mandatory to prevent  
25 electrolyte leakage, evaporation of solvents and delamination of the substrates (*i.e.* substrates  
26 traction and thermal expansion) <sup>[77,78]</sup>. In the case of a DSSM, the sealant must mainly protect the  
27 conductors from the corrosive action of the liquid electrolyte. Here, the width dedicated to the  
28 protection of the conductor was equal to 3.6 mm (1.8 mm on both sides of the conductor). The next  
29 fabrication step was the electrolyte injection by the vacuum backfilling technique through the pre-  
30 cut slits (Figure 1) in the sealing gasket <sup>[79]</sup>. To this end, a specially designed vacuum chamber  
31 (Figure 2,d) was employed, which allows simultaneous filling of ten modules, placed in vertical  
32 position.  
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34 The fabricated modules were assembled/laminated in strings and panels to protect all components  
35 from environment contamination, and to guarantee sturdiness and longevity of the devices <sup>[3]</sup>  
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(Figure 2 and Figure S3). The lamination process must not take place above a temperature of 90–100 °C to avoid degradation effects on dye, electrolyte and sealant. Three modules were connected in series by overlapping the bus bars to realize a string. Three strings were then connected in parallel with a conductive tin ribbon soldered on the bus bars. Before the lamination process, two cables were applied on the positive and negative contact of the panel. For the lamination, the panel (total surface  $59 \times 61 \text{ cm}^2$ ) was sandwiched in between two 4 mm-thick tempered glass sheets, and two 1.52 mm-thick PVB (polyvinyl butyral) foils. The glass/PVB/Panel/PVB/glass stack was inserted in a silicon bag under vacuum and then in an autoclave (10 m<sup>2</sup>/h throughput) at controlled pressure and temperature for 2 h, according to a procedure adopted in architectural, automotive, and ballistic applications. PVB guarantees strong binding, optical clarity, toughness, adhesion and adaptation to devices and irregularity of the surface, but it is generally cured at temperatures above 150 °C at more than 5 bar. Therefore, a lengthy optimization of the process parameters (pressure, temperature, ramping and time) proved necessary to avoid device or glass cracks (above 3.5 bar), dye or electrolyte degradation (above 90–100 °C) and incomplete polymerization (less than 2 h processing time). This challenging work demonstrated how a well-established and industrially relevant process could be adapted to the production of a DSSP of significant surface. Then, in the final fabrication step, the edge of the panel was sealed with silicone glue and a metal frame (Figures 2,h and Figure S4) to give the finished product.

## 2.3 Modules and panel performance

### 2.3.1 Transparent photovoltaic assessment

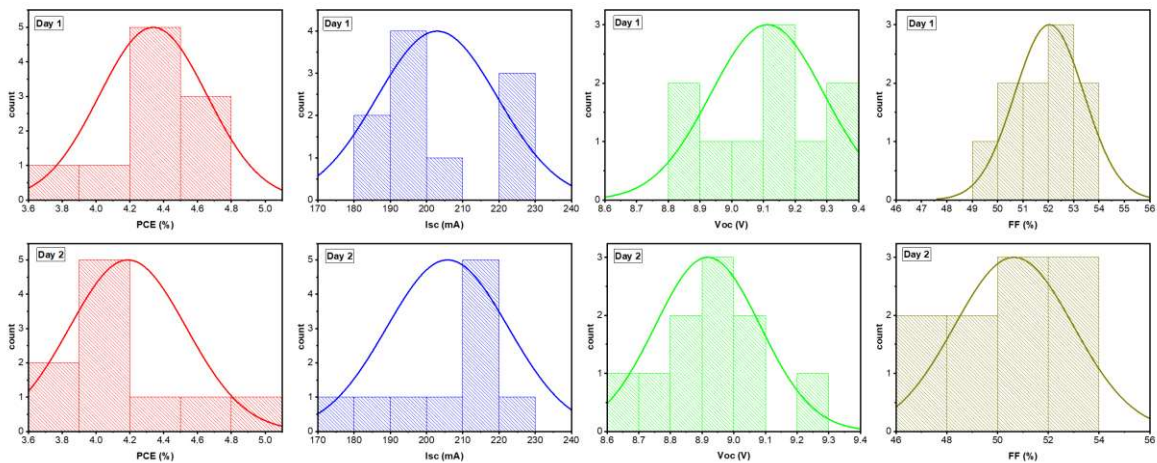
Light transmittance, i.e. transparency (AVT), is a physical property of a material that quantifies the amount of light allowed to pass through it without interruption [29,80]. AVT is a mandatory assessment for windows industry to evaluate the transparency level of the transparent PV technology [18,35]. The transparency of the module aperture area on the entire visible range was calculated to be 35.7% (see Equations (S1) and (S2) in the Supporting Information) by considering the transparency of the different sections of the module (sealant, 69.9% on 73.4 cm<sup>2</sup>; cells, 27.6% on 222 cm<sup>2</sup>; grids, 0% on 20 cm<sup>2</sup>) [57,71]. The value is higher compared to the reported literature about DSSC modules [3,53] and perfectly in agreement with BIPV needs [10,18,21,23,24,81]. Besides AVT, the color rendering index (CRI) is an important parameter for building interior space comfort [10,82]. It describes how accurately the color of an object is rendered through a transparent medium [10,35,82–84]. The module active area has  $x,y$  chromaticity coordinates of 0.454 and 0.339 in the CIE1931 diagram [35,84,85]. In this case, the CRI value is equal to 22.1%, as calculated according to

1 previous studies [84–86]. Considering the CRI value of the different sections of the module (sealant,  
 2 91%; cells, 22.1%; grids, 0%), the CRI of the module aperture area is 36.7%.

3 Traverse *et al.* introduced the light utilization efficiency ( $LUE = PCE \times AVT$ ) to compare different  
 4 technologies against theoretical limits and represents an overall (aesthetic and electrical) system  
 5 4 efficiency [10,18]. We got a LUE value of 1.8% and 1.0% on active and aperture area, respectively.  
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 7 5 The values are sufficient to self-power low-power mobile electronic devices and smart windows or  
 8  
 9 6 complement passive window coatings or smart window technologies [18].

### 15 9 2.3.2 Module testing

17 Since each string is composed of three modules connected in series (voltages of the modules are  
 18 10 summed), it is important to choose modules with comparable currents, because the worst module  
 19 11 will drive the string performance (Figure S5). In the following step, the strings are connected in  
 20 12 parallel (currents of the strings are summed) to form a panel, so the worst voltage string defines the  
 21 13 voltage of the panel. Therefore, during devices sorting, modules in the same string must have  
 22 14 comparable currents and the sum of the  $V_{oc}$  in each string must be similar (Figure S5). The  
 23 15 described procedure guarantees to obtain balanced strings and panel.  
 24 16  
 25 14 A total of 20 modules were assembled during two working days, providing very similar  
 26 15 performances in terms of average PCE (4.34% vs 4.19%), Isc (203 mA vs 205.8 mA), Voc (9.1 V vs  
 27 16 8.9 V) and FF (52% vs 50.7%), testifying the repeatability of the fabrication process (Figure 3 and  
 28 17 Table 2).



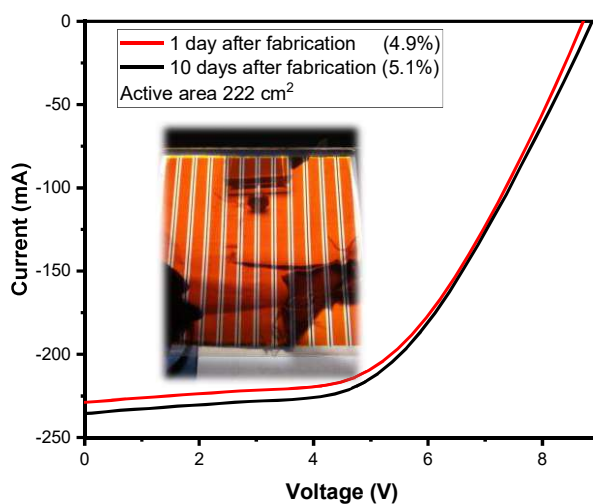
21  
 22 **Figure 3.** Electrical parameters distribution (histograms are fitted with a Gaussian distribution) of  
 23 the 20 modules fabricated along two days (10 per day).

**Table 2.** Average values and standard deviation of the electrical parameters of the 20 modules fabricated in two different days.

	PCE (%)	I <sub>sc</sub> (mA)	V <sub>oc</sub> (V)	FF (%)
<b>Day 1</b>	4.34±0.32	203±16.7	9.1±0.2	52±1.4
<b>Day 2</b>	4.19±0.36	205.8±16.8	8.9±0.2	50.7±2.3

The average Voc of the modules (9 V) corresponds to a cell voltage (module voltage/number of cell) of 0.75 V, that is in agreement with the Voc of a DSSC realized with the same materials [71].

The maximum module efficiency recorded was 4.9% with a Voc just below 9 V (Figure 4).



**Figure 4.** *I/V* plot of the best module.

After ten days from the module assembling, the module efficiency increased till 5.1%. The improvement is related to the penetration of the electrolyte in the TiO<sub>2</sub> matrix, as mentioned below in the discussion on the thermal resistance tests (see Section 2.4). As expected, the FF (54%) is comparable to the value (53%) reached by large area cells (3.6 cm<sup>2</sup>) fabricated with the same materials [54], because the cells width and the distance between the end of the cell and the collecting grid/bus bar are comparable between the two devices. The efficiency value decreased when moving from 0.5 to 3.6 and to 222 cm<sup>2</sup>, since the resistive losses of the TCO are directly proportional to the width of the cell and the distance between the cells in case of a module [41]. EIS analysis reported for the large area cells revealed low charge transfer resistance at TiO<sub>2</sub>/dye/electrolyte interface and high electron lifetime in case of the TTZ5 dye [54]. The latter explains the high Voc obtained with the modules in this work.

### 2.3.3 Panel testing

We measured the PV properties of the panel (60° tilted respect to the ground) in outdoor conditions at different hours on 1<sup>st</sup> of June 2021, on the roof of the CNR Research Area in Sesto Fiorentino (Italy) (Table 3). The incident sun power was assessed based on the data provided by the meteorological station of CNR-IBE, located on the roof of the same building where the measurements were made. The weather station setup is available online <sup>[87]</sup>.

**Table 3.** Electrical parameters of the DSSP at different sun power and related temperatures. Panel tilt 60°; active area 0.2 m<sup>2</sup>.

CEST	Sun irradiance (W/m <sup>2</sup> )	Temperature (°C)	P <sub>max</sub> (W)	Isc (mA)	Voc (V)	PCE (%)
12.30	940	25.0°C	3.37	350.11	25.88	1.80
15.00	870	27.0°C	3.75	409.16	26.50	2.16
16.00	730	27.2°C	3.86	416.03	26.42	2.65
17.00	730	26.8°C	3.57	383.94	26.35	2.49

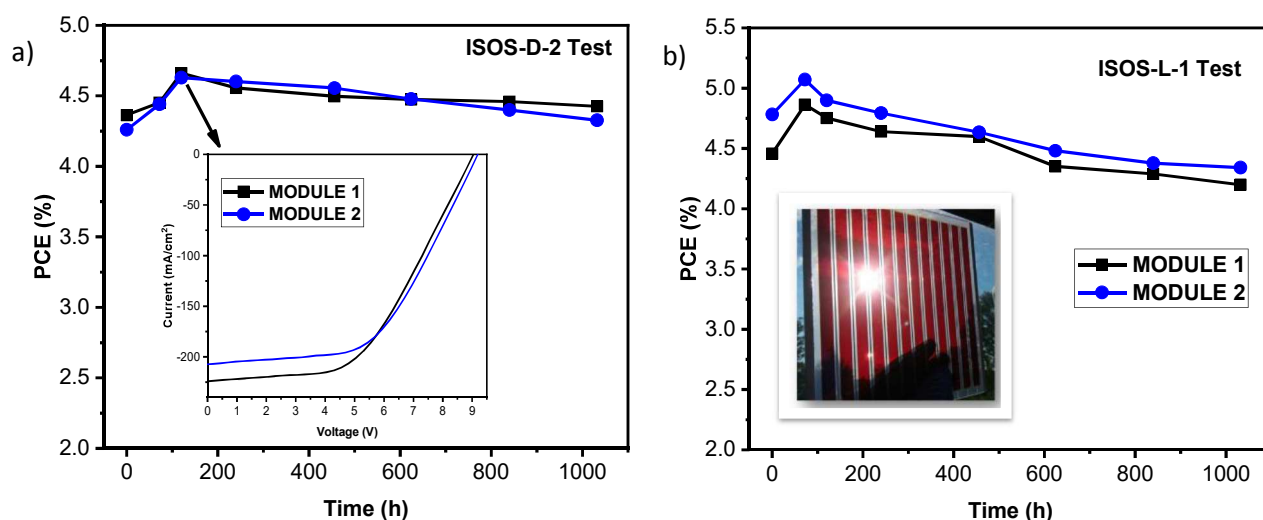
The highest efficiency (2.65%) was recorded at low irradiation intensities (730 W/m<sup>2</sup>), because in such conditions the diffusion of redox species in the electrolyte becomes a less limiting factor compared to high irradiation intensities <sup>[16]</sup>. The panel voltage was about 26.5 V, which corresponds to the parallel connection of three strings formed by three series-connected modules with about 9 V each (Figure S5).

### 2.4 Stability tests

Stability and durable lifetime are fundamental aspects to be addressed in a DSSC technology upscaling route <sup>[44]</sup>. Reliable DSSC modules need to show a lifetime ranging from 5 years (low-cost electronic applications) to 20 years (power-plant or BIPV). There are intrinsic and extrinsic degradation mechanisms related to dye, electrolyte, platinum and titania, but a large area device should be stable at molecular, cell and module level <sup>[47]</sup>. Temperatures above 50 °C have a detrimental effect on DSSC devices durability for different reasons: dye dissolution in the electrolyte <sup>[78]</sup>; dye desorption from TiO<sub>2</sub> layer <sup>[88]</sup>; irreversible loss of I<sub>2</sub> and I<sub>3</sub><sup>-</sup> <sup>[89]</sup>; electrolyte

1 leakage and degradation of the CE [42,88,90,91]. Moreover, sealant failure and delamination further  
 2 boost the degradation mechanisms.

3  
 4 Since specific certification protocols for DSSC technology have not been identified, ISOS  
 5 protocols, IEC 61215, JIS C8938 and JIS C8991 can be adopted [3,61,92]. We performed an ISOS-D-  
 6 2 test (dry conditions at 85 °C) in oven at Voc and an ISOS-L-1 test (light soaking at MPP-  
 7 Maximum Power Point, see Experimental Section) on four modules (two for each test) not used for  
 8 the panel (Figure 5).



9 **Figure 5.** a) ISOS-D-2 and b) ISOS-L-1 stability test of four different DSSMs.

10 After 120 h thermal stress, the modules increased the efficiency about 8% compared to the initial  
 11 value (Figure 5a). Such performance improvement is a phenomenon already discussed in the  
 12 literature [93,94]: the temperature reduces the electrolyte viscosity that helps the penetration in the  
 13 mesoporous TiO<sub>2</sub>. This improves the ionic diffusion and a fast dye chemical reduction, boosting FF  
 14 and current. After 1000 h, the efficiency was still about 3% higher than the initial value, although it  
 15 was reduced by about 6% respect to the highest value after 120 h. Since the results are comparable  
 16 with the same thermal test on cells [54], we can confirm that module connections, sealing and  
 17 fabrication process do not add any stability issue [95]. Moreover, we noticed no electrolyte leakage,  
 18 grids corrosion, empty liquid electrolyte zones and moiré patterns on the sealing part (Figure S6).  
 19 The thermal expansion of the glass substrates and electrolytes, and the sealant strain are the main  
 20 issues related to the mechanical sturdiness of DSSMs [42,44,52,76,91]. Researchers designed and  
 21 adopted a wide cell width (18 mm) to reduce the difference in thermal expansion [52,76]. Here, we  
 22 obtained stable DSSMs with a cell width of 10.25 mm by the optimized process engineering. In  
 23 Figure S2, we show how the gap between the CE and the PE did not change considerably after the

1 thermal stress at 85 °C. We tested two modules under continuous and constant illumination at 1 Sun  
2 and MPP (Figure 5b). The PCE progressively increased during the first 70 h up to 5.1%, then it  
3 started to drop, albeit with losses smaller than 10% and 15% (after 1000 h) compared to the initial  
4 and champion value, respectively. As for the ISOS-D-2 test, the modules did not present any issue  
5 related to sealing failures or grids corrosion.  
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### 10 6 11 12 7 **3. Conclusion** 13

14 The architectural versatility and the very good low-light performance highlight the full potential of  
15 8 DSSC technology in the BIPV industry [96]. Besides DSSM efficiency, transmittance should be  
16 9 carefully considered in BIPV sector, as the solar transmission also influences the building energy  
17 10 consumption. The materials composing the cell play a key role both in terms of efficiency and  
18 11 transparency. Since the window transparency influences the lighting condition of the interior and  
19 12 the energy consumption, thick TiO<sub>2</sub> layers provide higher PCEs compared to thin layers, but  
20 13 without improving the overall efficiency of the building [9]. The most suitable dye for outdoor  
21 14 installation must be resistant to temperatures above 50 °C to avoid dye dissolution in the electrolyte  
22 15 and dye desorption from TiO<sub>2</sub> layer. In this study, we focused on the design and manufacturing of  
23 16 modules, strings and panels by considering the three main features of a potential BIPV installation:  
24 17 efficiency, transparency and durability of the devices. The best efficiency on active area is 5.1%  
25 18 (aspect ratio above 70%), the transparency is 35.7% (LUE above 1.8%) and the modules are stable  
26 19 at 85 °C (ISOS-D-2 test) and under light soaking (ISOS-L-1 test) for more than 1000 h, thanks to  
27 20 the gap uniformity between PE and CE and the reliable sealing process. After sorting 20 highly  
28 21 reproducible modules by considering the series connection to build a string and the parallel  
29 22 connection to realize a panel, we laminated a 0.2 m<sup>2</sup> panel according to an industrially compatible  
30 23 method. The panel has a maximum efficiency of 2.7% in outdoor conditions with 60° tilt angle. If  
31 24 compared to the reported literature in the introduction, the devices have similar dimensions and  
32 25 exhibit comparable efficiency to our previous work, but they featured an in-house developed  
33 26 organic dye and did not present any change in the used materials respect to the stability test  
34 27 performed. Moreover, the AVT is higher than any previous reported semi-transparent DSSM. We  
35 28 also showed the fabrication, the design and the aspect ratio details of the devices, from module to  
36 29 panel. Finally, we reported stability tests of semi-transparent modules, filling a literature gap. The  
37 30 reported procedure can pave the way for a scalable, systematic and stable module/panel fabrication  
38 31 process on large scale.  
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## 1 4. Experimental Section

2 The full fabrication process took place in ambient air.

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5 3 *Module Fabrication:* FTO (fluorinated tin oxide)-covered glass sheets (Pilkington TEC7, 2.2 mm  
6 thick) were cut in a size of 19.2×20 cm<sup>2</sup> and bevelled on the edge. Then, the substrates were  
7  
8  
9 5 patterned to form 12 series connected cells by a nanosecond raster scanning laser ( $\lambda=1064$  nm,  
10 Nd:YVO<sub>4</sub>) and cleaned with soap, acetone and ethanol, successively. This step was crucial to have  
11  
12 7 good adhesion of the subsequently deposited materials and to avoid contamination. A silver ink  
13  
14 8 (Chimet, 3700 80%) was deposited by a semi-automatic screen-printer (Baccini-Applied Materials)  
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16 9 with 77.48 mesh to realize the conductor grids and the bus-bars. The substrates were dried at 120°C  
17  
18 10 for 30 min (Lenton WHT6/60 oven) before the deposition by screen-printing technique of TiO<sub>2</sub>  
19  
20 11 (GreatCell Solar 18-nrt with 43.80 mesh) and of Pt (3D Nano Pt-106 with 100.40 mesh) inks on  
21  
22 12 PEs and on CEs, respectively. The PEs were inserted in a box for 5 min to guarantee the material  
23  
24 13 levelling and smoothness of the layer. Then PEs were sintered at 500 °C for 30 min (2.5 h ramping)  
25  
26 14 and CEs were fired at 480 °C for 30 min (2 h ramping). Ten PEs at a time were heated at 100 °C for  
27  
28 15 15 min, dipped vertically in the dye solution (0.2 mM TTZ5 in THF) while still hot, and kept for 15  
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30 16 h, typically overnight, to let the dye molecules be absorbed onto the mesoporous TiO<sub>2</sub> surface. After  
31  
32 17 soaking, PEs were rinsed with THF and Ethanol and dried in air. Both absorption and cleaning  
33  
34 18 processes were carried out in a laboratory room where the UV component of the light was filtered.  
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36 19 A Bynel™ thermoplastic gasket (80 μm thick) was shaped according to the module layout by a  
37  
38 20 cutting plotter (Smallone V). Then, the gasket was stuck without melting on the CE by a thermal  
39  
40 21 press (Memo 50) at 1.6 bar/125 °C for 60 sec. PE and CE were sealed together and aligned to series  
41  
42 22 connecting the grids, and the thermoplastic foil became transparent after the polymerization (1.8  
43  
44 23 bar/165 °C for 70 sec). The spacing of the electrodes was about 60 μm. The HSE electrolyte  
45  
46 24 (triiodide/iodide redox couple in 3-methoxypropionitrile, with inorganic/organic iodide salt and an  
47  
48 25 imidazole derivative as additives) from Great Cell Solar was injected in 10 modules by the designed  
49  
50 26 vacuum back filling system through the 60 μm slits (one per cell) shaped in the gasket when  
51  
52 27 preparing the thermoplastic foil. Then, the slits were sealed by positioning under the press (1.8  
53  
54 28 bar/180 °C for 80 sec) the edge of the module to avoid thermal and pressure overexposure of the  
55  
56 29 polymer in the main part of the device.

57  
58 30 *Panel fabrication:* After module sorting, 3 modules were connected in series by overlapping the  
59  
60 31 bus-bars to realize a string. Three strings were connected in parallel with a conductive tin ribbon  
61  
62 32 soldered on the bus bars. Then, two cables were applied on the positive and negative contact. The  
63  
64 33 panel (58.3 × 60 cm<sup>2</sup>) was sandwiched in between two 4 mm thick tempered glass and two 1.52 mm



1 thick PVB (polyvinyl butyral) foils and then inserted in a silicone bag under vacuum. The bag was  
2 placed in an industrial autoclave (Bombi) at 3.3 bar/100 °C (40 min/60 min ramping) for few  
3 minutes. The full process lasted 2 h. Then, the edge of the panel was sealed with silicone glue and a  
4 metal frame (final dimensions 59 cm × 61 cm × 1.8 cm).

5 *Characterization:* The film profiles were measured using a profilometer (DEKTAK 150, Veeco  
6 Instruments Inc.). The procedure to measure the gap between the two assembled electrodes was as  
7 follows. We marked 9 points on the two substrates (Figure S2): the bottom substrate with a scribe  
8 parallel to the silver fingers and the top substrate with a scribe orthogonal to the previous one to  
9 form a cross. With the confocal microscope (OLS-4000 system, Olympus) we detected the focal  
10 planes in correspondence of the scribe in the top substrate and in the bottom substrate. Then, we  
11 calculated the distance of the two scribes (i.e. gap/thickness of the chamber) along the z-axis  
12 measuring the difference between the absolute values of the two focal planes. The optical analysis  
13 related to dye concentration, AVT and CRI was performed with a UV-vis spectrometer (UV-2550,  
14 Shimadzu). AVT and CRI measurements correspond to a complete device assessment and were  
15 determined according to the Supporting Information and to the literature [80,84–86]. The photovoltaic  
16 characteristics of the modules were measured with a class B sun simulator (KHS, Solar Constant  
17 1200) at AM 1.5 1000W/m<sup>2</sup> calibrated with a SKS 1110 sensor (Skye Instruments Ltd.); the system  
18 was equipped with a 2612 source meter (Keithley Instruments Inc.) and a LabVIEW interface.  
19 During the measurement a UV filter was placed on top of the module. The light soaker (Arkeo  
20 CICCI Research) is a low mismatch cool white LED based system (400-750 nm). A  
21 LabVIEW/Python based code manages the J/V and MPP tracking state. A standard perturb-and-  
22 observe tracking algorithm was selected for the devices. The photovoltaic characterization of the  
23 panel was performed in outdoor conditions on the 1<sup>st</sup> of June 2021 in Sesto Fiorentino (Italy, exact  
24 coordinates: N 43° 49' 07'', E 11° 12' 04''), on a mostly sunny day. J/V curves were measured with  
25 a B2901A digital source meter (Keysight Technologies). The incident sun power was assessed  
26 based on the data provided by the meteorological station of CNR-IBE, located on the roof of the  
27 same building where the measurements were made [87].

## 28 **Supporting Information**

29 Supporting Information is available from the Wiley Online Library

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## TOC Graphic

### Process Engineering of Semi-transparent DSSC Modules and Panel Incorporating an Organic Sensitizer

*L. Vesce, P. Mariani, M. Calamante, A. Dessì, A. Mordini, L. Zani\*, A. Di Carlo\**

We demonstrate a reliable and reproducible method to fabricate under ambient air conditions several large-area dye-sensitized solar modules (400 cm<sup>2</sup>) and a panel (0.2 m<sup>2</sup>) containing an organic dye. The finest module efficiency is more than 5% (35.7% AVT) with thermal (ISOS-D-2) and light stability (ISOS-L-1). An industrially-compatible process was adopted to laminate the panel, which showed an outdoor efficiency of 2.7%.

